



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shunpei YAMAZAKI et al. Art Unit : 2813
Serial No. : 09/769,765 Examiner : Laura Schillinger
Filed : January 26, 2001
Title : SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450


REQUEST FOR INITIALED PTO FORM 1449

Upon reviewing the file, applicants noted that they have not received an initialed copy of the enclosed PTO Form 1449 that accompanied an information disclosure statement filed January 26, 2001 (courtesy copy, along with a copy of the postcard enclosed).

Applicants' records show that this information disclosure statement complied with 37 CFR 1.97. Thus, we respectfully request that the examiner initial and return this form as soon as possible. Note, applicants must pay the issue fee by September 15, 2004, so the examiner's prompt attention to this matter would be appreciated.

Respectfully submitted,

Date: 8/2/04

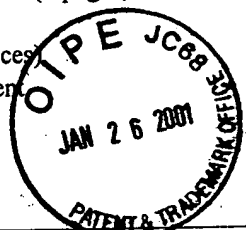


Roberto J. Devoto
Reg. No. 55,108

Fish & Richardson P.C.
1425 K Street, N.W.
11th Floor
Washington, DC 20005-3500
(202) 783-5070 telephone
(202) 783-2331 facsimile

COPY

Attorney's Docket No. 12732-008001	Express Mail Label No.	Mailing Date January 26, 2001	For PTO Use Only <i>Do Not Mark in This Area</i>	
Application No. Not yet assigned	Filing Date January 26, 2001	Attorney/Secretary Init JFH/WDH/nth		
Title of the Invention SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF				
Applicant YAMAZAKI et al.				
Enclosures <ul style="list-style-type: none">• Transmittal Letter (in duplicate, 2 pages)• Patent Application (55 pages of specification, 13 pages of claims, 47 total claims, 1 pages of abstract)• Drawings (informal, 16 sheets)• Information Disclosure Statement (1 pages)• Form PTO-1449 (1 pages)• Prior Art References (3 references)• Transmittal of Priority Document				





IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

COPY

Applicant : YAMAZAKI et al. Art Unit : Unknown
Serial No. : Not yet assigned Examiner : Unknown
Filed : January 26, 2001
Title : SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

Commissioner for Patents
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Applicant submits the references listed on the attached form PTO-1449, copies of which are enclosed.

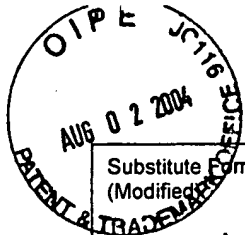
This statement is being filed with the application. Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: January 26, 2001

William D. Hare
William D. Hare
Reg. No. 44,739

Fish & Richardson P.C.
601 Thirteenth Street, NW
Washington, DC 20005
Telephone: (202) 783-5070
Facsimile: (202) 783-2331



COPY

Sheet 1 of 1

Substitute Form PTO-1449
(Modified)

U.S. Department of Commerce
Patent and Trademark Office

Attorney's Docket No.
12732-008001

Application No.
Not yet assigned

**Information Disclosure Statement
by Applicant**

(Use several sheets if necessary)

(37 CFR §1.98(b))

Applicant
YAMAZAKI et al.

Filing Date
January 26, 2001

Group Art Unit
Unknown

U.S. Patent Documents

Examiner Initial	Desig. ID	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	5,643,826	July 1, 1997	Ohtani et al.			
	AB	5,923,962	July 13, 1999	Ohtani et al.			
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							

Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
	AQ	Japanese Patent Application Laid-Open No. 10-048640A (English Abstract attached)
	AR	
	AS	
	AT	

Examiner Signature

Date Considered

EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.